

i. MX6u1traLite versions comparison

Features	MCIMX6G0	MCIMX6G1	MCIMX6G2	MCIMX6G3
Speed	528 MHz	528 MHz	528 MHz	528 MHz
CACHE	32KB-I,32KB-D 128 KBL2	32KB-I,32KB-D 128 KBL2	32KB-I,32KB-D 128 KBL2	32KB-I,32KB-D 128 KBL2
OCRAM	128 KB	128 KB	128 KB	128 KB
DRAM	16 bit LP-DDR2 • DDR3/DDR3L	16bit LP-DDR2 • DDR3/DDR3L	16bit LP-DDR2 • DDR3/DDR3L	16bit LP-DDR2 • DDR3/DDR3L
User eFuse	512bit	1024bit	1536bit	2048bit
NAND(BCH40)	Y	Y	Y	Y
Paralleled NOR/EBI	Y	Y	Y	Y
Ethernet	10/100 MB×1	10/100 MB×1	10/100 MB× 2	10/100 MB×2
USB with PHY	OTG • HS/FS×1	OTG • HS/FS×2	OTG • HS/FS×2	OTG • HS/FS×2
CAN	0	1	2	2
Security	Basic functions	TRNG ,Encryption engine (AES/TDES/SHA) , Safety Guide	TRNG ,Encryption engine (AES/TDES/SHA) ,Safety Guide	TRNG ,Encryption engine (AES+ DPA /TDES/SHA/ RS A) , Safety Guide, Tamper Detection,PCI4.0 Pre authentication ,OTF DRAM Encryption
Graphic	N	N	P×P	P×P
CSI	N	N	24bit paralleled CSI	24bit paralleled CSI
LCD	N	N	24bit paralleled CSI	24bit paralleled CSI
Quad spl	1	1	1	1
SDIO	2	2	2	2
UART	4	8	8	8
I ² C	2	4	4	4
SPI	2	4	4	4
I ² S/SAI	1	3	3	3
S/PDIF	1	1	1	1
Timer/PWM	2 Timer • 4 PWM	4 Timer • 8 PWM	4 Timer • 8 PWM	4 Timer • 8 PWM
12bit ADC	1×8	1×8	2×8	2×8

- The red color means changes compared the left row